FIG.1 CONVENTIONAL ART

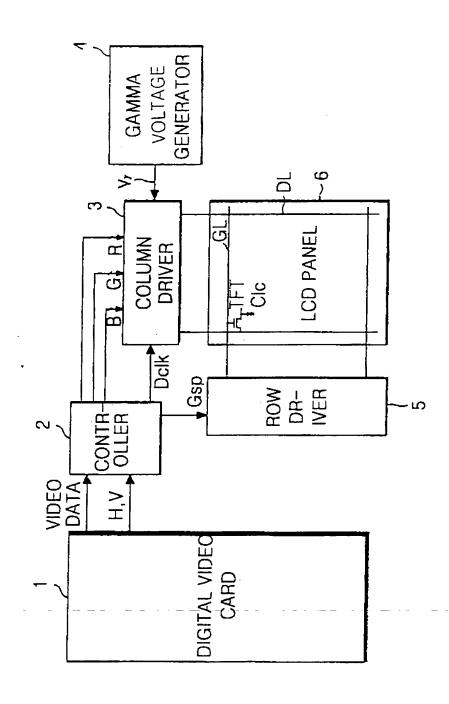


FIG.2 CONVENTIONAL ART

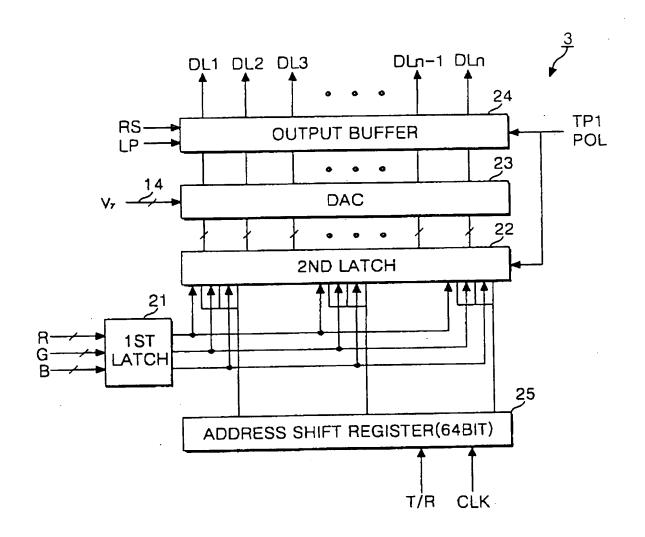


FIG.3 CONVENTIONAL ART

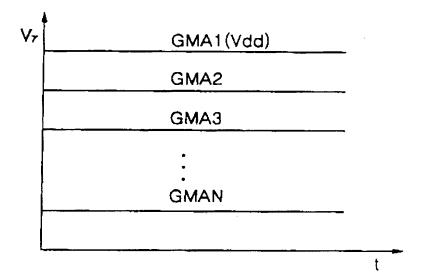


FIG.4 CONVENTIONAL ART

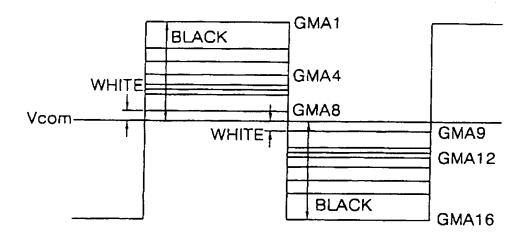
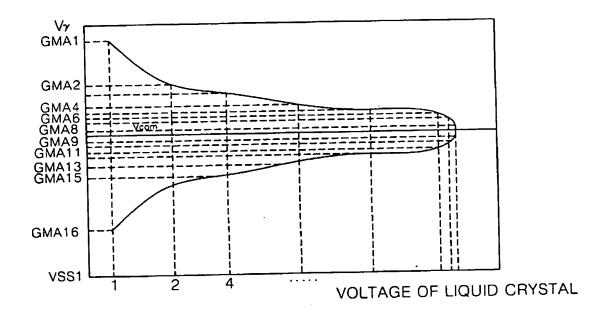


FIG.5 CONVENTIONAL ART



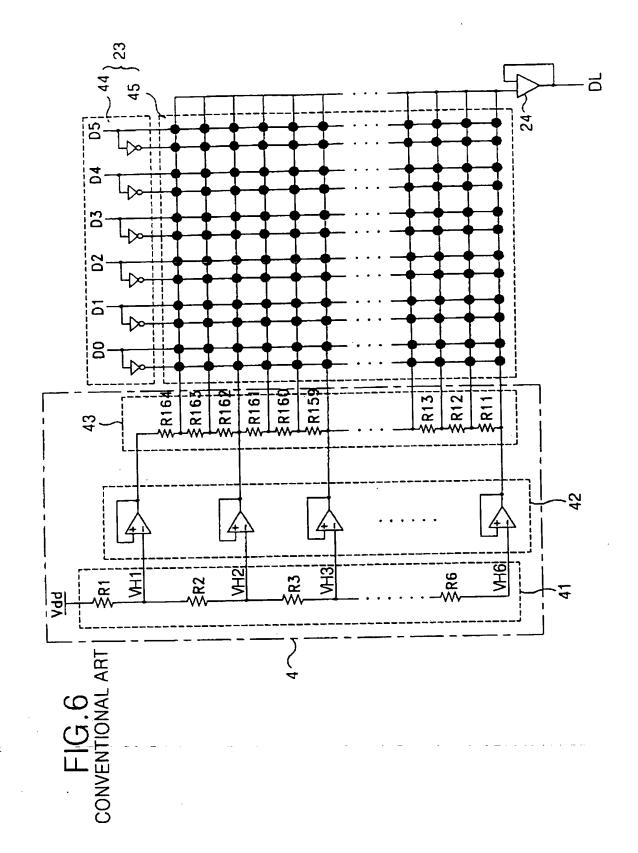


FIG.7 CONVENTIONAL ART

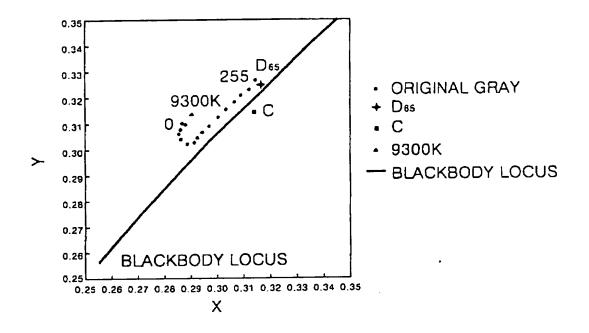
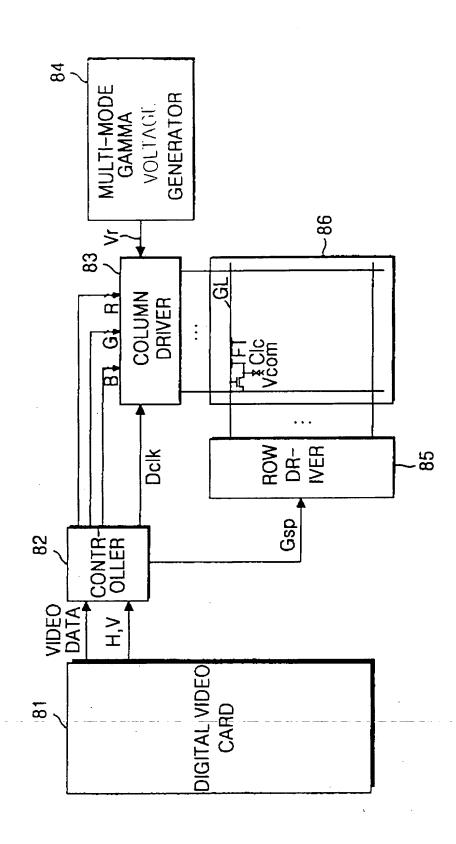
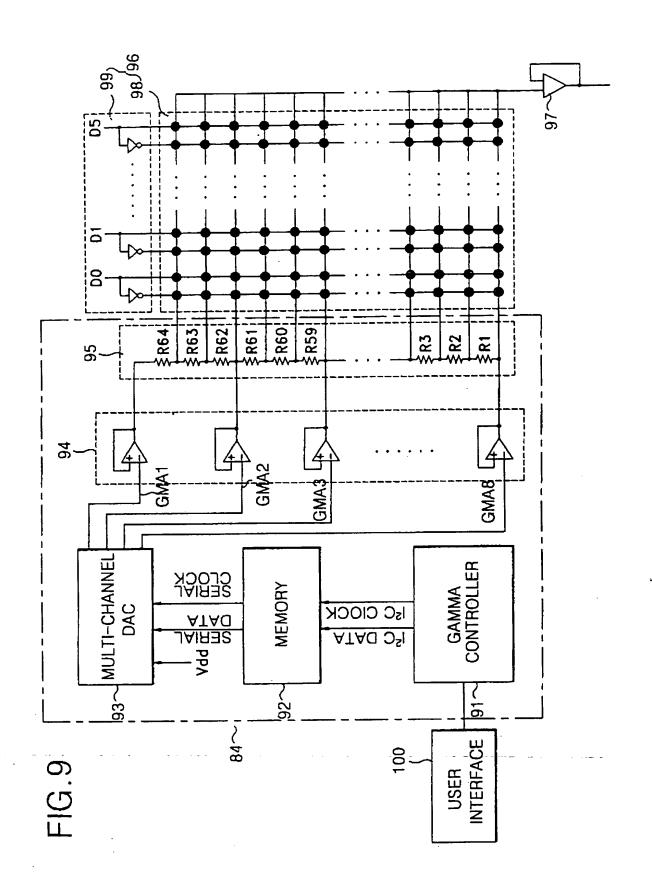


FIG.8





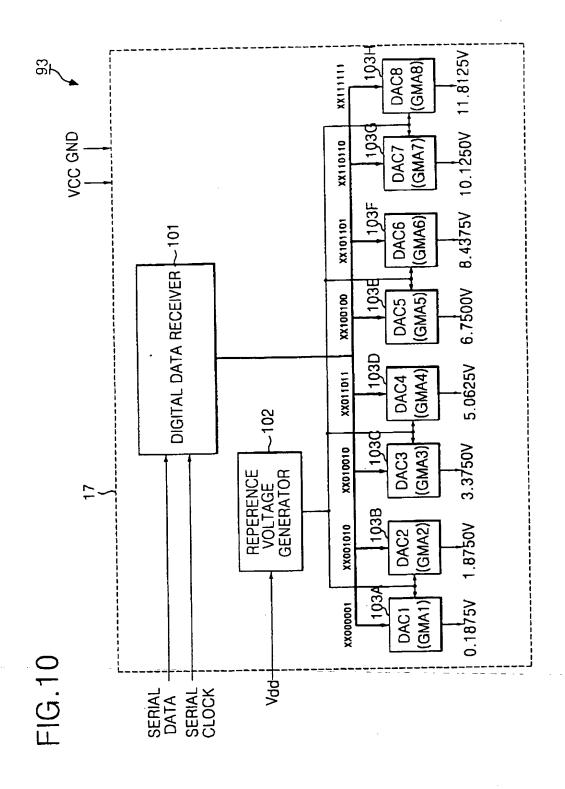
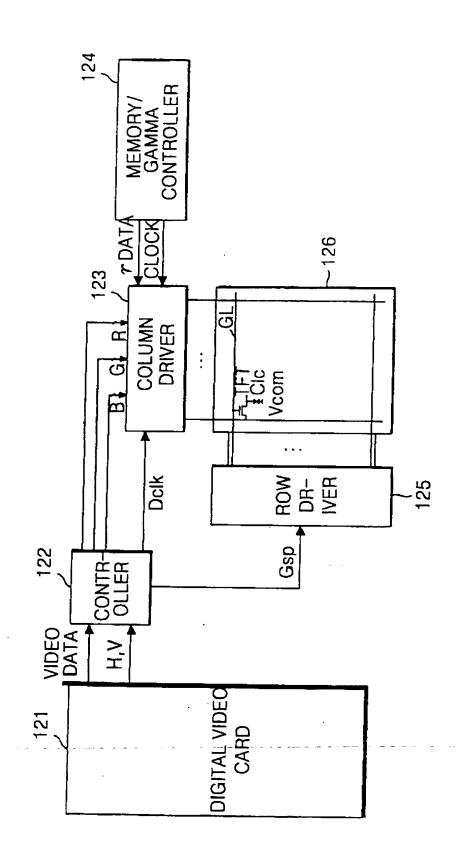


FIG. 11

X X D5 D4 D3 D2 D1 D0	
4	
A3 A2 A1 A0 SD SC SB SA	
တ	

S=START CONDITION, A3~A0=ADDRESS BIT, SC~SA=SUBADDRESS BIT D5~D0=DATA BIT A=HEADER BIT,

FIG.12



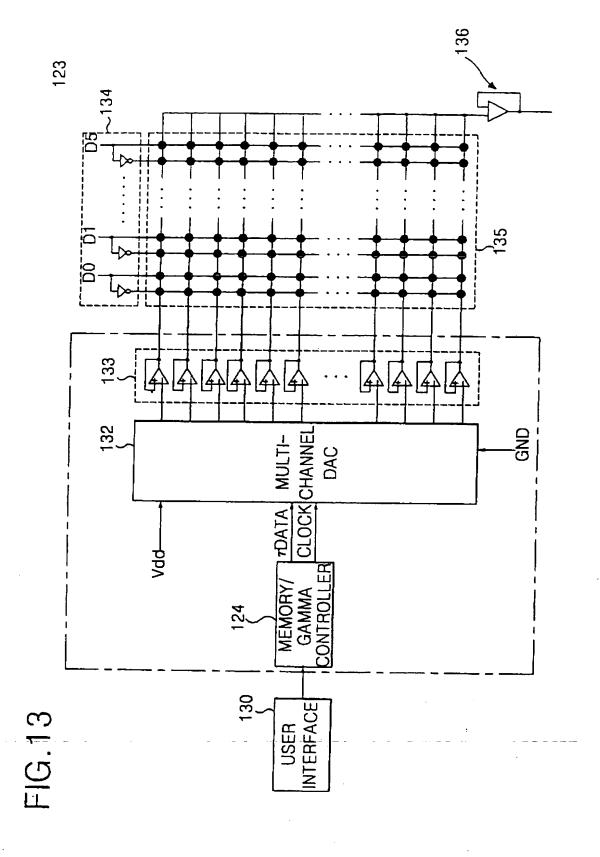
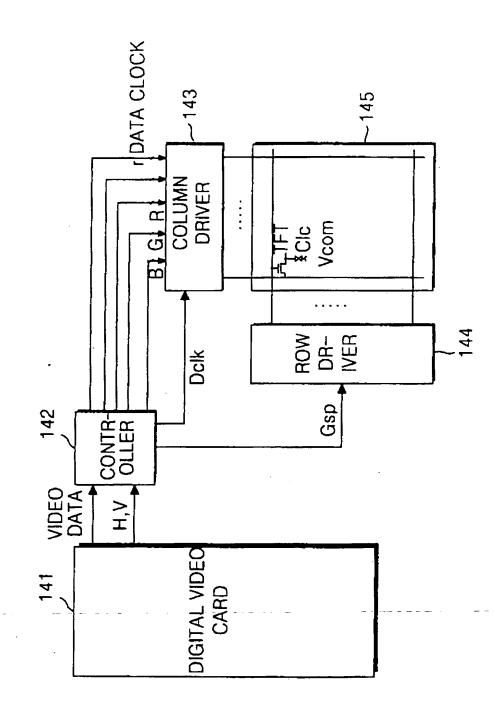
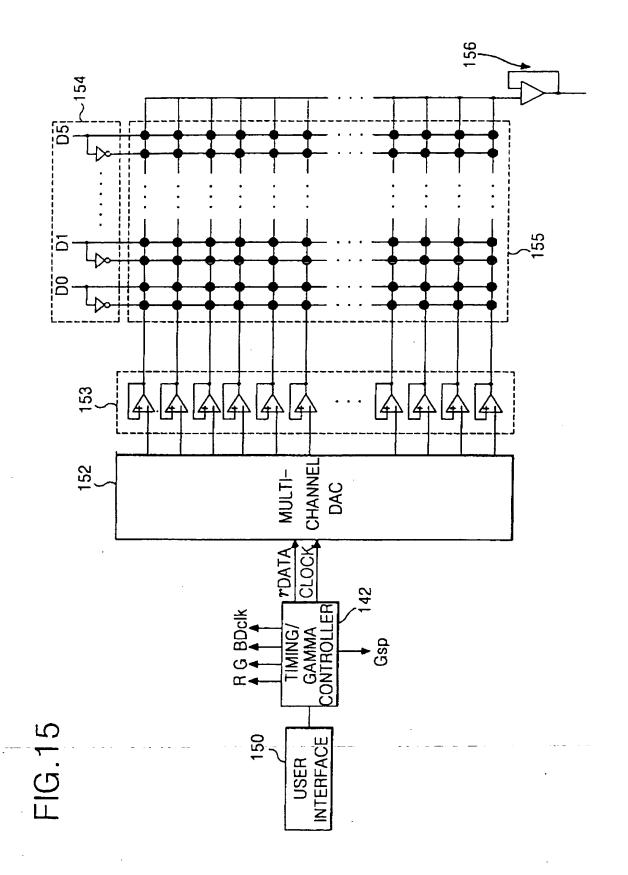


FIG.14





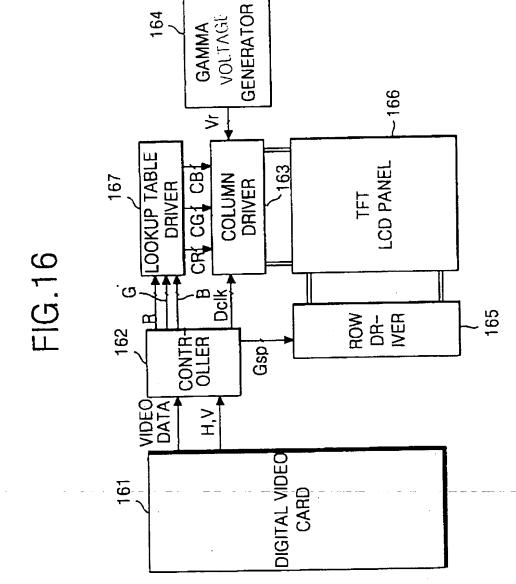


FIG.17

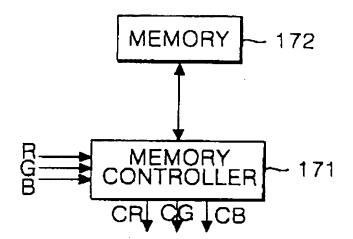


FIG.18

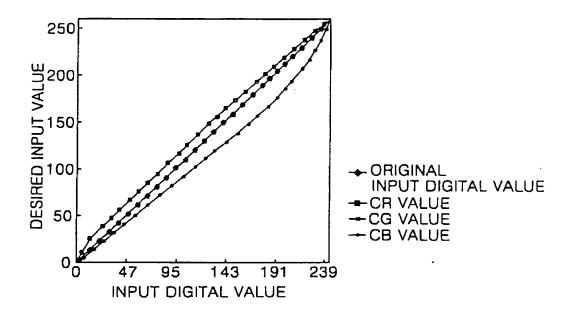


FIG.19

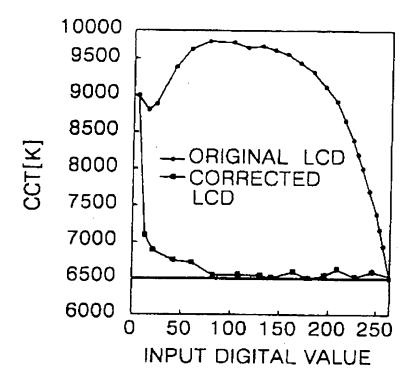
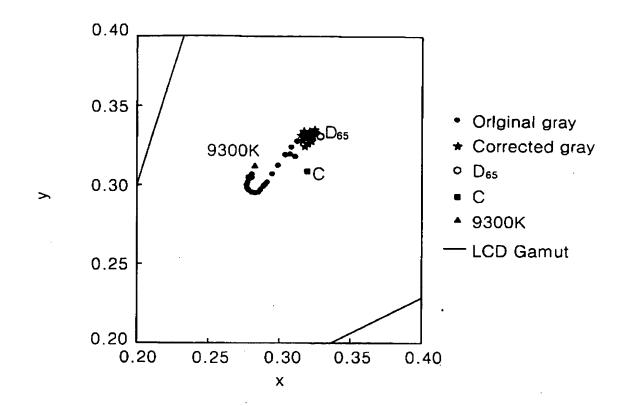
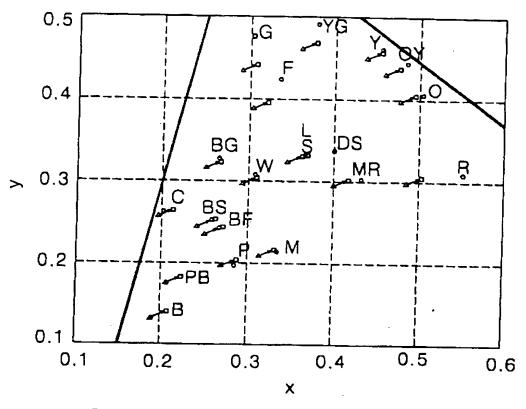


FIG.20



FORGE LOGEOROG

FIG.21



- . CHROMINANCE CO-ORDINATES OF INPUT IMAGE
- **△ CHROMINANCE** CO-ORDINATES OF LCD DISPLAY IMAGE
- CHROMINANCE CO-ORDINATES CORRECTED BY LOOKUP TABLE